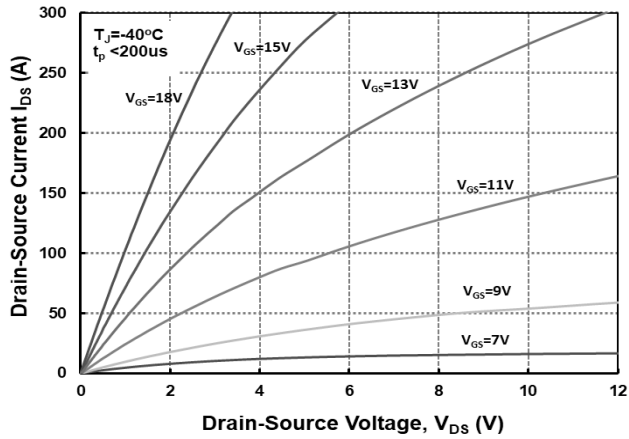
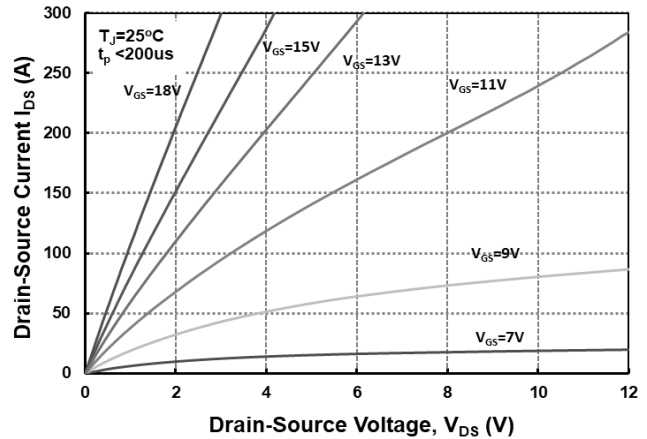
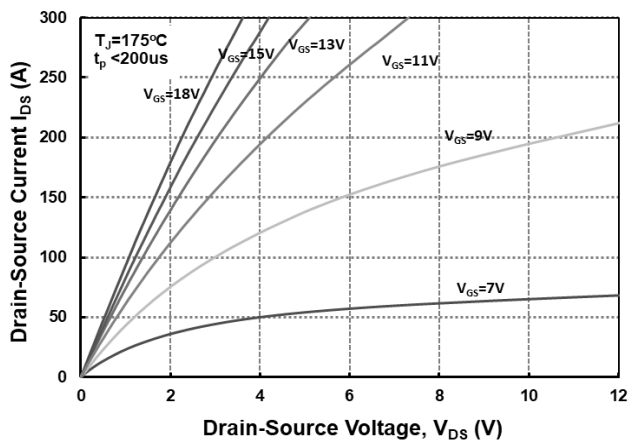
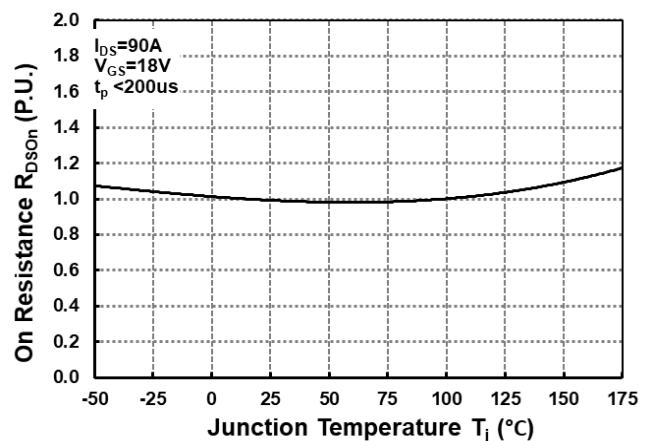
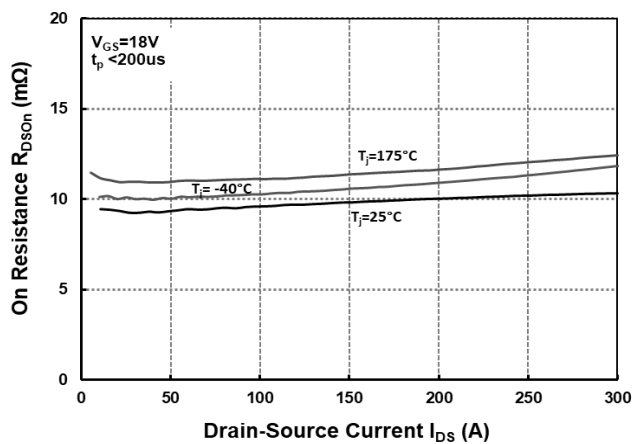
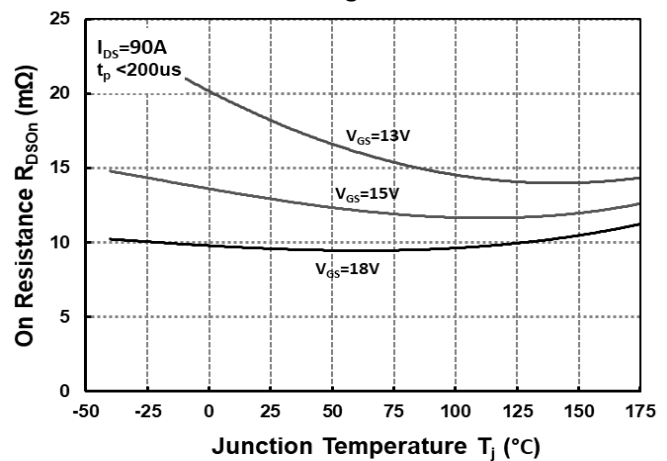


Electrical Characteristics @ T_c =25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
OFF Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =0.1mA	650	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =650V, V _{GS} =0V	-	0.5	60	μA
		V _{DS} =650V, V _{GS} =0V, T _J =175°C	-	5	200	
Gate-Source Leakage Current	I _{GSS}	V _{GS} =18V, V _{DS} =0V	-	5	100	nA
		V _{GS} =-4V, V _{DS} =0V	-100	-5	-	
ON Characteristics						
Gate Threshold Voltage **	V _{GS(th)}	V _{DS} =V _{GS} , I _D =30mA	2.6	3.1	4.2	V
		V _{DS} =V _{GS} , I _D =30mA, T _J =150°C	-	2.4	-	
		V _{DS} =V _{GS} , I _D =30mA, T _J =175°C	-	2.3	-	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =18V, I _D =90A	-	10	14	mΩ
		V _{GS} =18V, I _D =90A, T _J =175°C	-	12	-	
Transconductance	g _{fs}	V _{DS} =20V, I _D =90A	-	78	-	S
		V _{DS} =20V, I _D =90A, T _J =175°C	-	70	-	
Internal Gate Resistance	R _{G(int.)}	f=1MHz, I _D =0A	-	8	-	Ω
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =400V V _{GS} =0V Freq.=100kHz V _{AC} =25mV	-	5300	-	pF
Output Capacitance	C _{oss}		-	430	-	
Reverse Transfer Capacitance	C _{riss}		-	40	-	
Coss Stored Energy	E _{oss}		-	45	-	μJ
Turn-On Switching Energy	E _{on}	V _{DS} =400V, V _{GS} =-4/+18V I _D =90A, R _{G(ext)} =2.0Ω, R _{G(int.)} =9Ω L=200μH	-	560	-	μJ
Turn-Off Switching Energy	E _{off}		-	450	-	
Switching Characteristics						
Turn-On Delay Time	t _{d(on)}	V _{DS} =400V, V _{GS} =-4/+18V I _D =90A, R _{G(ext)} =2.0Ω, R _{G(int.)} =9Ω, L=200μH	-	45	-	ns
Rise Time	t _r		-	60	-	
Turn-Off Delay Time	t _{d(off)}		-	128	-	
Fall Time	t _f		-	30	-	
Total Gate Charge	Q _g	V _{DS} =400V V _{GS} =-4/+18V I _D =90A	-	268	-	nC
Gate to Source Charge	Q _{gs}		-	73	-	
Gate to Drain Charge	Q _{gd}		-	110	-	
Body Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _{SD} =50A, T _J =25°C	-	3.3	-	V
		V _{GS} =0V, I _{SD} =50A, T _J =175°C	-	2.9	-	
Continuous Diode Forward Current	I _S	V _{GS} =-4V, T _J =25°C	-	125	-	A
Reverse Recovery Time	T _{rr}	I _{SD} =90A, V _{GS} =-4V V _R =400V, L=200μH dif/dt=1639A/μs	-	38	-	ns
Reverse Recovery Charge	Q _{rr}		-	770	-	nC
Peak Reverse Recovery Current	I _{rrm}		-	43	-	A
Thermal Resistance						
Thermal Resistance, Junction-to-Case	Rθ _{JC}		-	0.25	-	°C/W

** Turn-off with -4V gate bias is highly recommended

Typical Performance

Fig 1. Output Characteristics, $T_J = -40^\circ\text{C}$

Fig 2. Output Characteristics, $T_J = 25^\circ\text{C}$

Fig 3. Output Characteristics, $T_J = 175^\circ\text{C}$

Fig 4. Normalized On-Resistance vs. Temperature

Fig 5. On-Resistance vs. Drain Current for Various Temperatures

Fig 6. On-Resistance vs. Temperature for Various Gate Voltage


Typical Performance

Fig 7. Transfer Characteristic for Various Junction Temperatures

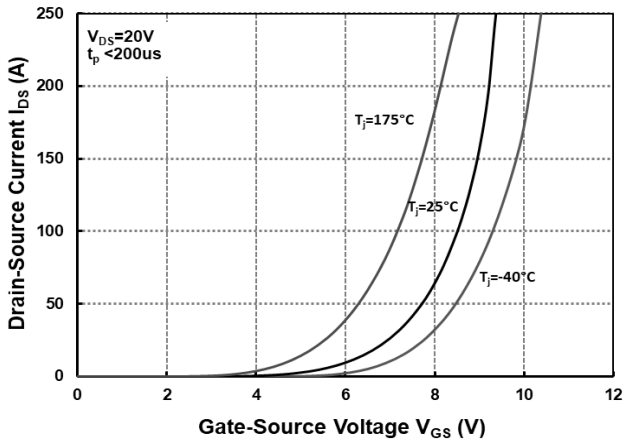


Fig 8. Body Diode Characteristics @ -40°C

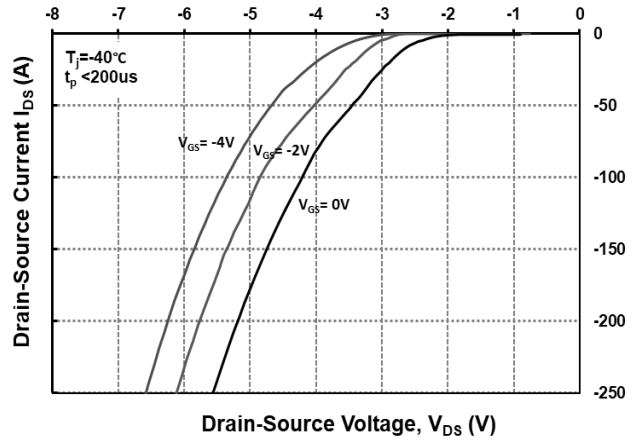


Fig 9. Body Diode Characteristics @ 25°C

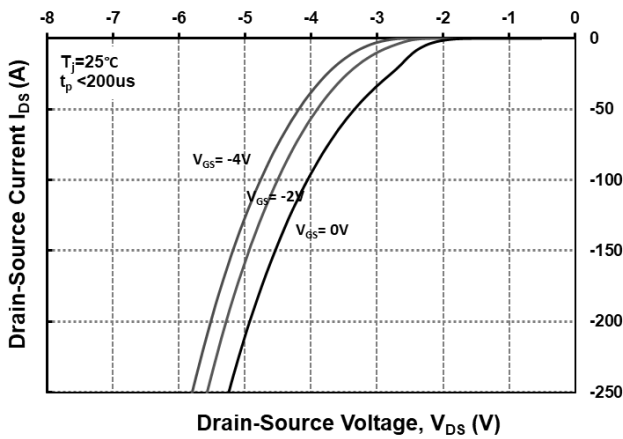


Fig 10. Body Diode Characteristics @ 175°C

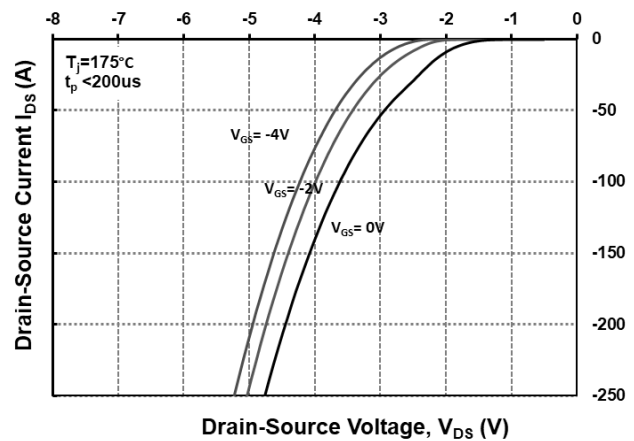


Fig 11. Threshold Voltage vs. Temperature

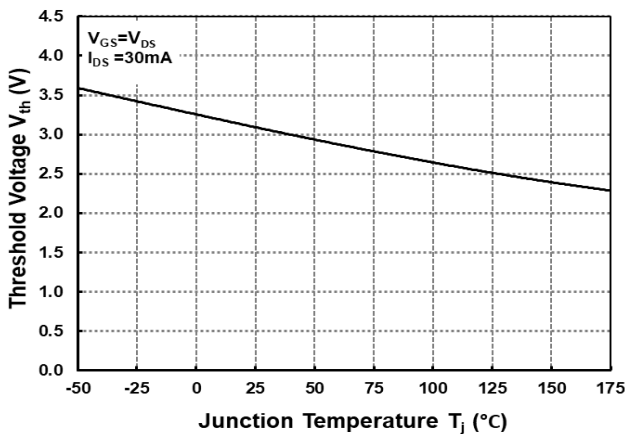
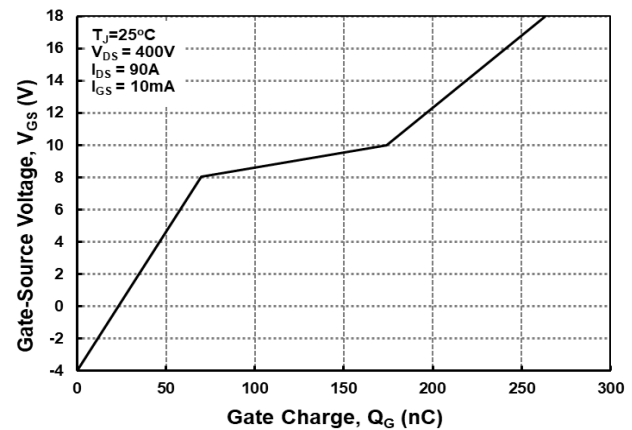
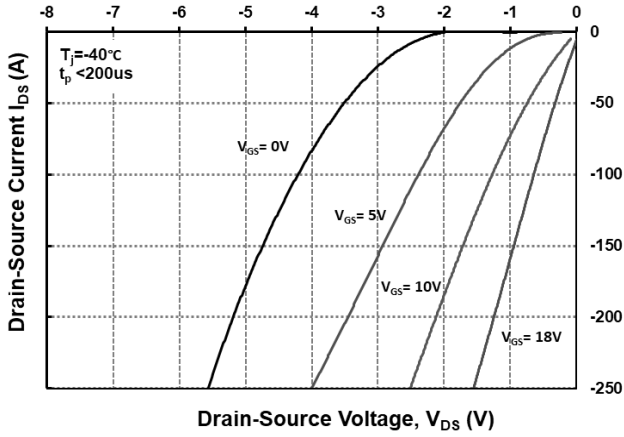
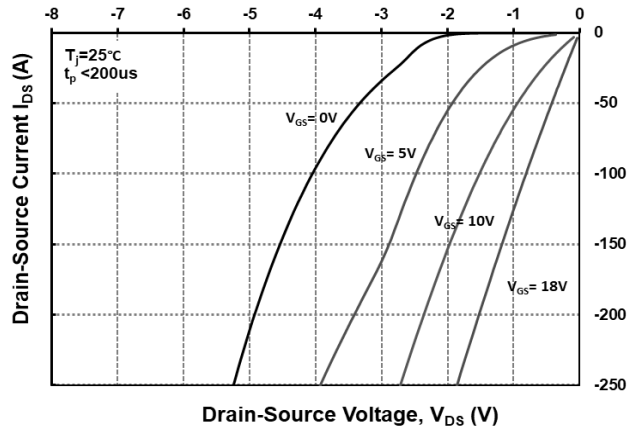
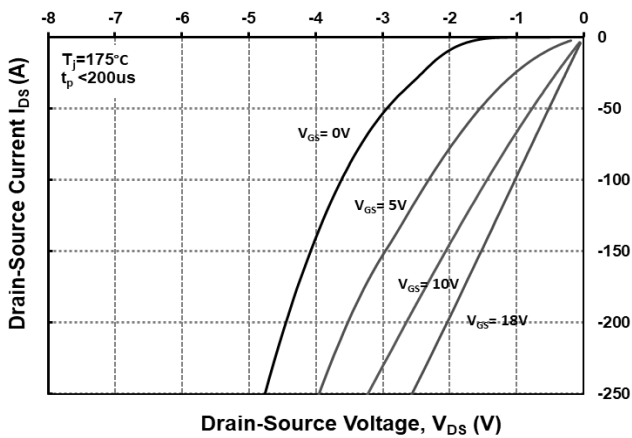
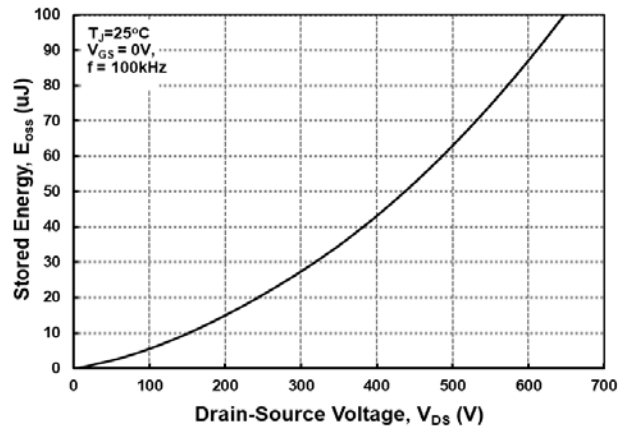
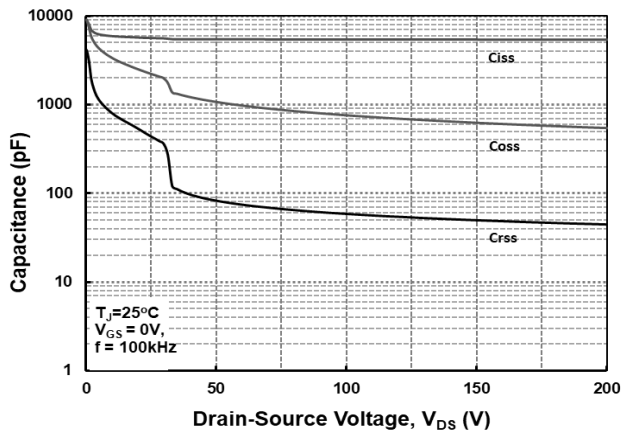
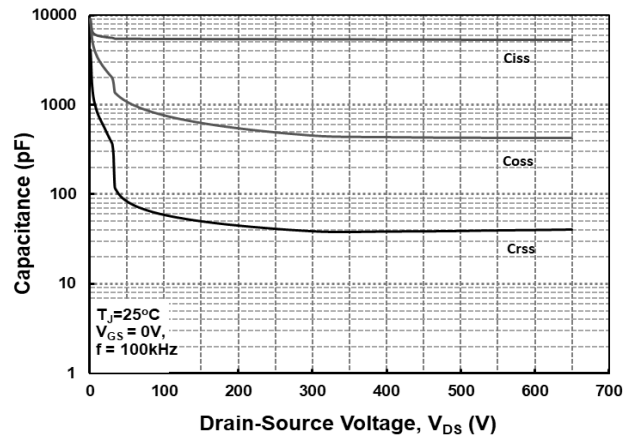


Fig 12. Gate Charge Characteristics



Typical Performance
Fig 13. 3rd Quadrant Characteristics @ -40°C

Fig 14. 3rd Quadrant Characteristics @ 25°C

Fig 15. 3rd Quadrant Characteristics @ 175°C

Fig 16. Output Capacitor Stored Energy

Fig 17. Capacitances vs. Drain-Source Voltage (0-200V)

Fig 18. Capacitances vs. Drain-Source Voltage (0-650V)


Typical Performance

Fig 19. Continuous Drain Current Derating vs. Case Temperature

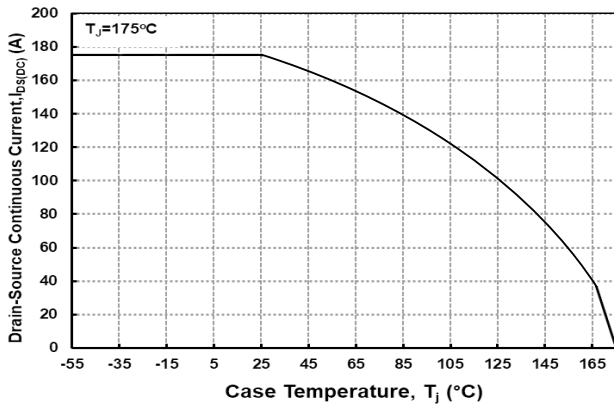


Fig 20. Maximum Power Dissipation Derating vs. Case Temperature

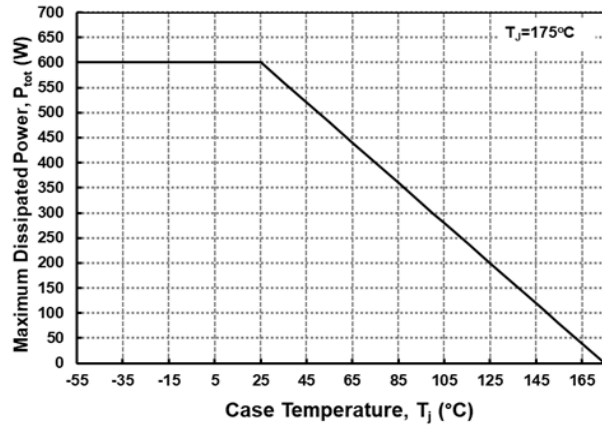


Fig 21. Transient Thermal Impedance (Junction-Case)

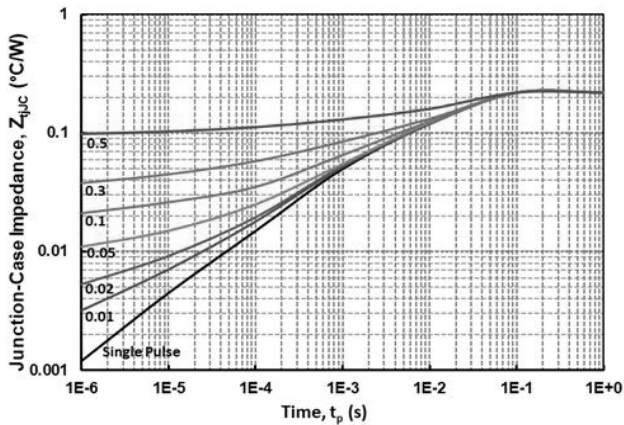


Fig 22. Safe Operating Area

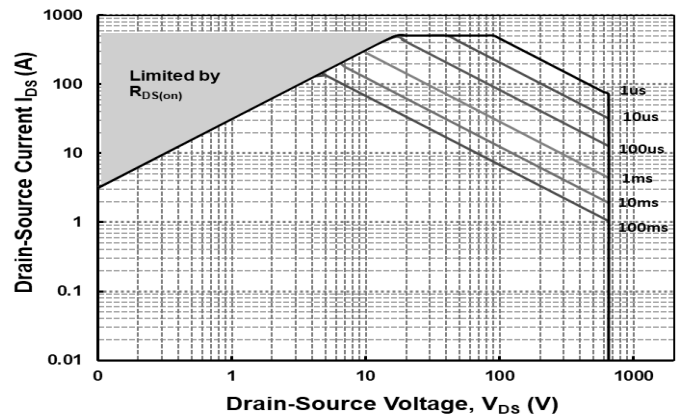


Fig 23. Clamped Inductive Switching Energy vs Drain Current ($V_{DD} = 400\text{V}$)

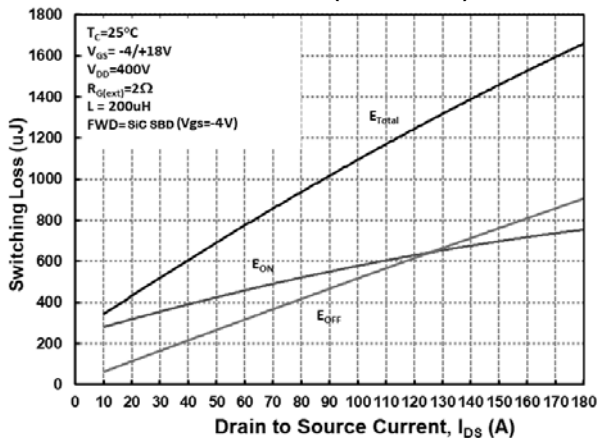
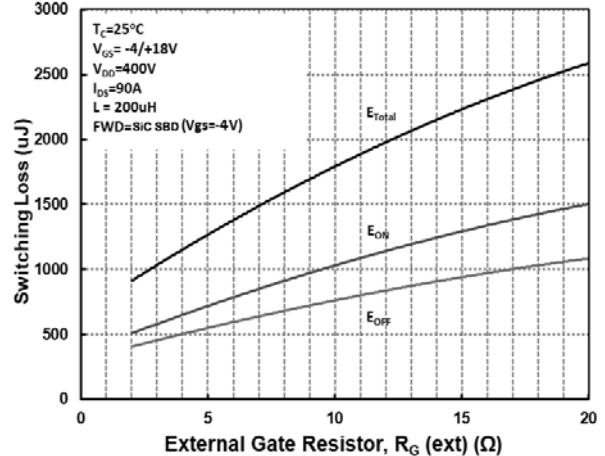
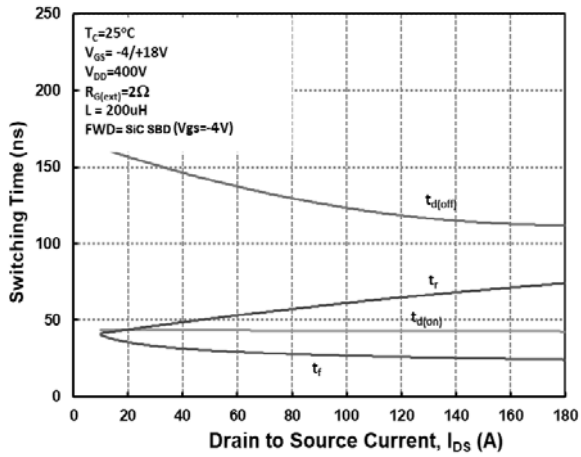
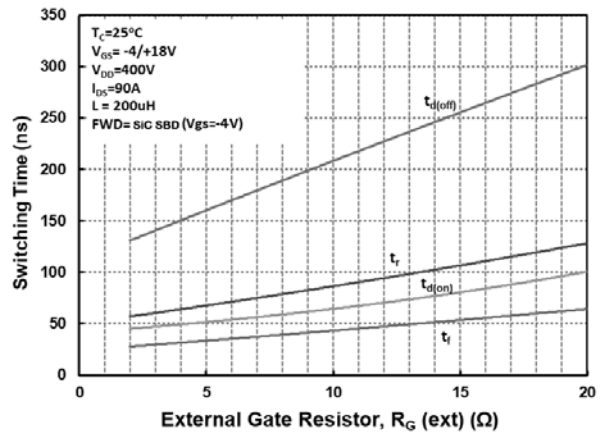


Fig 24. Clamped Inductive Switching Energy vs External Gate Resistor $R_{G(ext)}$



Typical Performance
Fig 25. Switching Times vs Drain Current
($V_{DD} = 400V$)

Fig 26. Switching Times vs External Gate Resistor $R_{G(ext)}$


Disclaimer

DACO Semiconductor reserves the right to make modifications, enhancements, improvements, corrections, or other changes to this document and any product described herein without prior notice. For the most up-to-date version, please visit our website.

DACO Semiconductor makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does DACO Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any liability, including without limitation special, consequential or incidental damages.

Purchasers are responsible for its products and applications using DACO Semiconductor products, including compliance with all laws, regulations, and safety requirements or standards, regardless of any support or application information provided by DACO Semiconductor. "Typical" parameters that may be provided in DACO Semiconductor datasheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typical" must be validated for each customer application by the customer's technical experts.

DACO Semiconductor products are not designed, authorized, or warranted to be suitable for use in life support, life-critical or safety-critical systems, or equipment, nor in applications where failure or malfunction of DACO Semiconductor's product can reasonably be expected to result in personal injury, death or severe property or environmental damage. DACO Semiconductor accepts no liability for the inclusion and/or use of DACO Semiconductor's products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Purchasers who buy or use DACO Semiconductor products for any unintended or unauthorized applications are required to indemnify and absolve DACO Semiconductor, its suppliers, and distributors from any claims, costs, damages, expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that DACO Semiconductor was negligent regarding the design or manufacture of the part.

No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or by any information storage and retrieval system, or otherwise, without the prior written permission of DACO Semiconductor Co., Ltd.